

Feb. 26. 2004 3:59PM QLOGIC

No. 4091 P. 4

Patent  
QN1072.US**DECLARATION  
Utility Application**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled **METHOD AND SYSTEM FOR REDUCING DEADLOCK IN FIBRE CHANNEL FABRICS USING VIRTUAL LANES** the specification of which

(Check One)

☒ is attached hereto OR

☐ was filed on \_\_\_\_\_ as United States Application Serial No. \_\_\_\_\_;  
PCT International Application No. \_\_\_\_\_  
and was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment(s) referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application Number(s)	Country	Date of Filing	Priority Claimed	
			Yes	No

I hereby claim the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below.

Application Number(s)	Filing Date
60/542,241	02/05/04

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s), or § 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

U.S. Parent Application Number	PCT Parent Number	Parent Filing Date	Status-Patented, Pending or Abandoned

Residence, post office address, citizenship and signature of inventor(s) set forth beginning on next page.

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I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements are made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18, United States Code, § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

**POWER OF ATTORNEY:** As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number)

Tejinder Singh (Reg. No. 39,535)  
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FULL NAME OF INVENTOR	FIRST Name STEVEN	MIDDLE Initial M.	LAST Name BETKER	
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INVENTOR'S SIGNATURE <i>Steven M. Betker</i>		DATE <i>Feb. 26, 2004</i>		

Patent  
QN1072.US**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE****In re the Application of:****STEVEN M. BETKER****Serial No.: To be assigned****Filed:****For: METHOD AND SYSTEM FOR  
REDUCING DEADLOCK IN FIBRE  
CHANNEL FABRICS USING VIRTUAL  
LANES****)**  
**) Group Art Unit:**  
**)**  
**) Examiner: To be assigned**  
**)****GRANT OF POWER OF ATTORNEY**Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

As representative of the assignee of an entire interest in the above-identified application, the undersigned hereby appoints as attorney for the assignee herein, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith, Howard Klein,

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**CERTIFICATE OF MAILING**  
(37 C.F.R. §1.10)

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as "Express Mail Post Office To Addressee" in an envelope addressed to the Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

3/11/2004  
Date of Deposit

CARRIE CHEUNG  
Name of Person Mailing Paper  
[Signature]  
Signature of Person Mailing Paper

Patent  
QN1072.US

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Respectfully submitted,

QLogic Corporation

Dated: February 26, 2004

By: Julia Schappals

Name: Julia Schappals

Title: General Counsel  
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